



THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Isamu Kobori et al.

Art Unit : 2811

Serial No.: 10/623,581

Examiner: Unknown

Filed

: July 22, 2003

Title

: METHOD OF MANUFACTURING A SEMICONDUCTOR METHOD OF

MANUFACTURING A THIN-FILM TRANSISTOR AND THIN-FILM

TRANSISTOR

Commissioner for Patents P.O. Box 1450

Alexandria, VA 22313-1450

REQUEST FOR CORRECTED OFFICIAL FILING RECEIPT

Please correct the Filing Receipt for the above-referenced application to include the correct number of drawings. The correct number of sheets of drawings is six (6).

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Respectfully submitted,

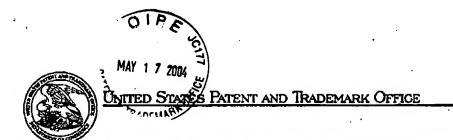
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| APPL NO. | FILING OR 371 (c) DATE | ART UNIT | FIL FEE REC'D | ATTY.DOCKET NO | DRAWINGS | TOT CLMS | IND CLIMS |
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| 10/623,581 | 07/22/2003 | 2811 | 1014 | 07977-024003 | 6× | - 30 | 8.4 |

CONFIRMATION NO. 6534 W. Finite W

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Applicant(s)

Isamu Kobori, Chiba, JAPAN; Michio Arai, Tokyo, JAPAN;

Assignment For Published Patent Application

Semiconductor Energy Laboratory Co., Ltd.; TDK Corporation:

Domestic Priority data as claimed by applicant

This application is a DIV of 09/016,999 02/02/1998 PAT 6,596,572 which is a DIV of 08/623,506 03/28/1996 PAT 5,767,529

Foreign Applications

JAPAN 7-96266 03/28/1995

If Required, Foreign Filing License Granted: 04/28/2004

Projected Publication Date: 08/05/2004

Non-Publication Request: No

Early Publication Request: No

Title

Method of manufacturing a semiconductor method of manufacturing a thin-film transistor and thin-film transistor

Preliminary Class

257

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